

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Robert G. Wodnicki : Group Art Unit:

Serial No.: To Be Assigned : Examiner:

Filed: Concurrently

Title: ALIGNMENT METHOD FOR FABRICATION OF
INTEGRATED ULTRASONIC TRANSDUCER ARRAY

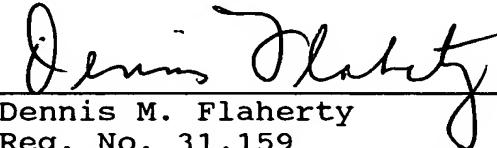
Hon. Commissioner of Patents & Trademarks
Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT

Sir:

Pursuant to 37 CFR § 1.97, the Applicant hereby requests that the reference cited in the accompanying Form PTO-1449 be considered by the examiner in the above-entitled application and made of record therein. A copy of the cited reference is submitted herewith.

Respectfully submitted,



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December 30, 2003
Date

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U.S. PATENT DOCUMENTS

EXAMINER INITIAL		PATENT NUMBER	ISSUE DATE	PATENTEE	CLASS	SUB-CLASS	FILING DATE
	A	6,384,516	5/7/02	Fraser	310	334	

PUBLISHED PATENT APPLICATIONS

EXAMINER INITIAL		PUB. NUMBER	PUBLICATION DATE	COUNTRY OR PATENT OFFICE	CLASS	SUB-CLASS	TRANSLATION	
							YES	NO

OTHER DOCUMENTS

(Including Author, Title, Date, Relevant Pages, Place of Publication)

Examiner:	Date Considered:
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.